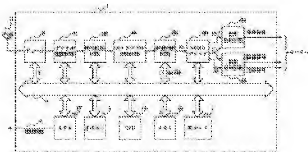


DIGITAL BROADCAST RECEIVER**Publication number:** JP10215441 (A)**Publication date:** 1998-08-11**Inventor(s):** KITA SACHIKAZU; MIHARA YOSHIKAZU; HARA NOBUHIRO; YAMAGATA TOYOMI; ATO HAJIME; AWANO SEIJI**Applicant(s):** SANYO ELECTRIC CO**Classification:****- international:** *H04N5/44; H04H20/00; H04H20/74; H04H40/18; H04N7/08; H04N7/081; H04N5/44; H04N7/08; H04N7/081;* (IPC1-7): H04N7/08; H04H1/00; H04N5/44; H04N7/081**- European:****Application number:** JP19970017997 19970131**Priority number(s):** JP19970017997 19970131**Abstract of JP 10215441 (A)**

PROBLEM TO BE SOLVED: To provide the digital broadcast receiver that increases/decreases only channels able to be viewed. **SOLUTION:** A CPU 58 extracts identification data as to free charge channels and contracted channels among identification data as to all channels based on key data or the like in an IC card 7 and stores the data to a memory 59. When a remote commander 6 sends a channel-up command or a channel-down command, the CPU 58 generates a tuning control signal T for a tuner 5 and a channel selection signal C for a multiplexer demultiplexer circuit 54 based on the identification data in the memory 59 and provides outputs of the signals T, C to the tuner 5 and the multiplexer demultiplexer circuit 54. Thus, the received channel is switched by taking only channels able to be viewed as objects.

Data supplied from the **esp@cenet** database — Worldwide